

[illegible]

FIG. 2 A

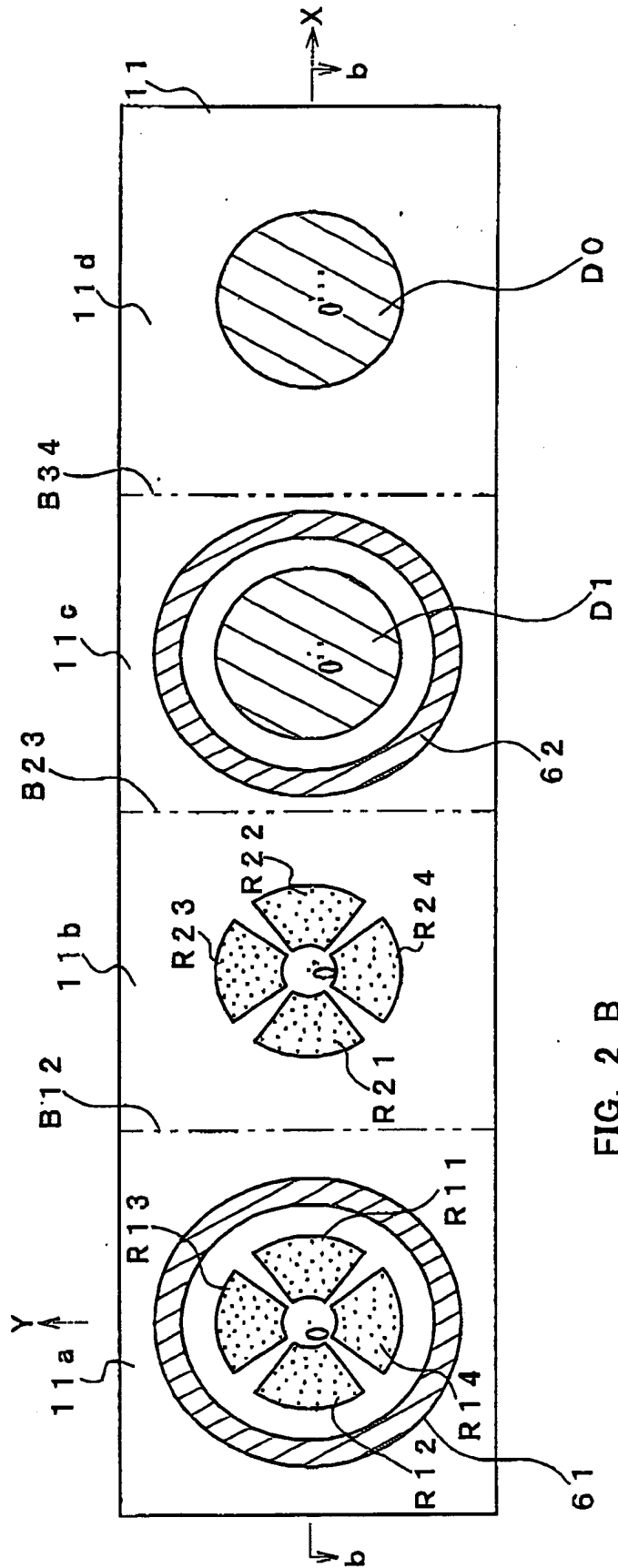


FIG. 2 B

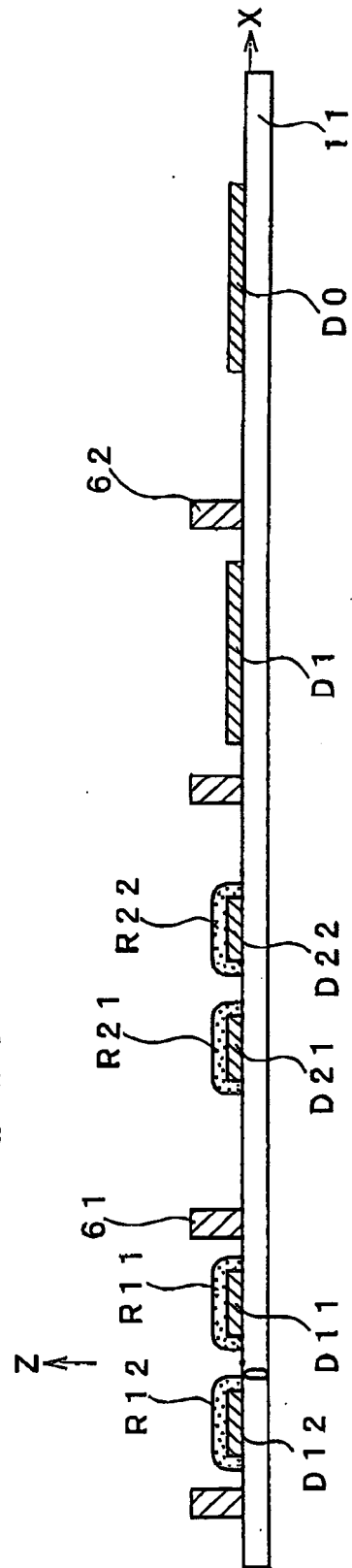
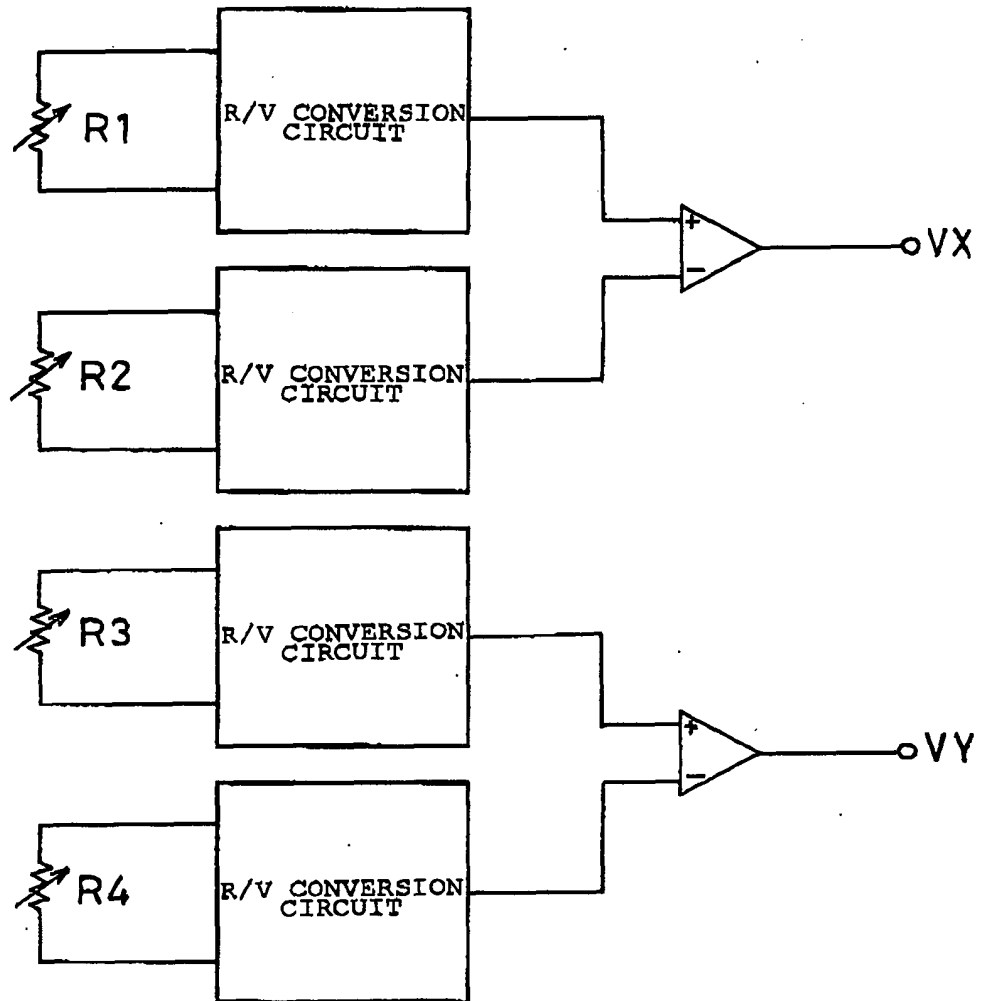


FIG. 3



The diagram shows a microcomputer system with two AD converters and two input ports. The microcomputer is represented by a dashed box labeled '5' containing a block labeled 'MICRO-COMPUTER INPUT PORT I'. The system is powered by V_{CC} and ground. The input ports are connected to the microcomputer via resistors $R1, R2, R3, R4$ and their respective pull-up/pull-down resistors $R10, R20, R30, R40$. The AD converters are connected to the microcomputer via resistors $R1, R2, R3, R4$ and their respective pull-up/pull-down resistors $R10, R20, R30, R40$. The output of the AD converters is connected to the microcomputer via resistors $R1, R2, R3, R4$ and their respective pull-up/pull-down resistors $R10, R20, R30, R40$. The output of the AD converters is connected to the microcomputer via resistors $R1, R2, R3, R4$ and their respective pull-up/pull-down resistors $R10, R20, R30, R40$.

FIG. 5

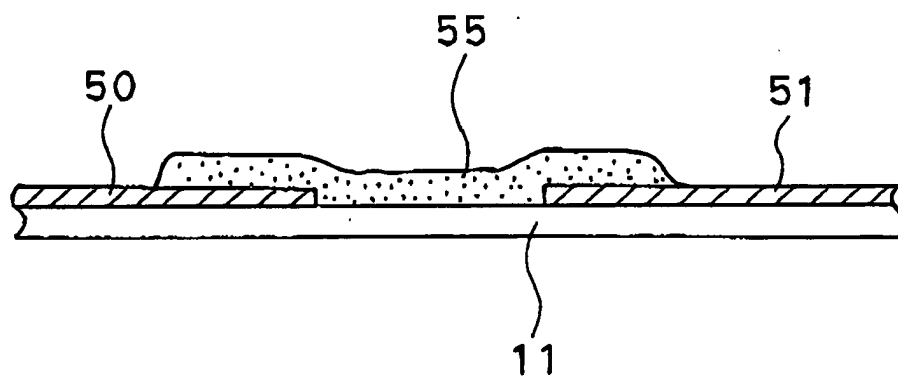


FIG. 6

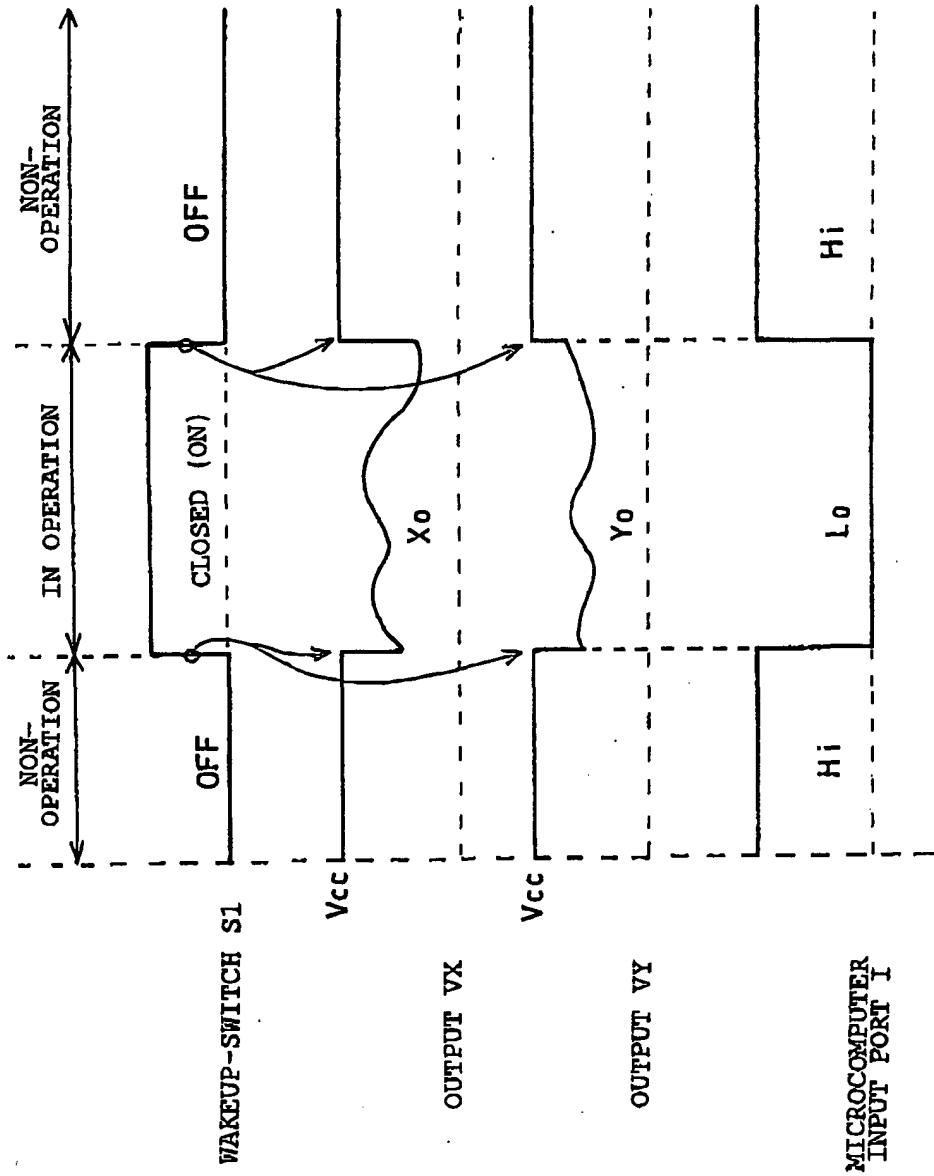


FIG. 7

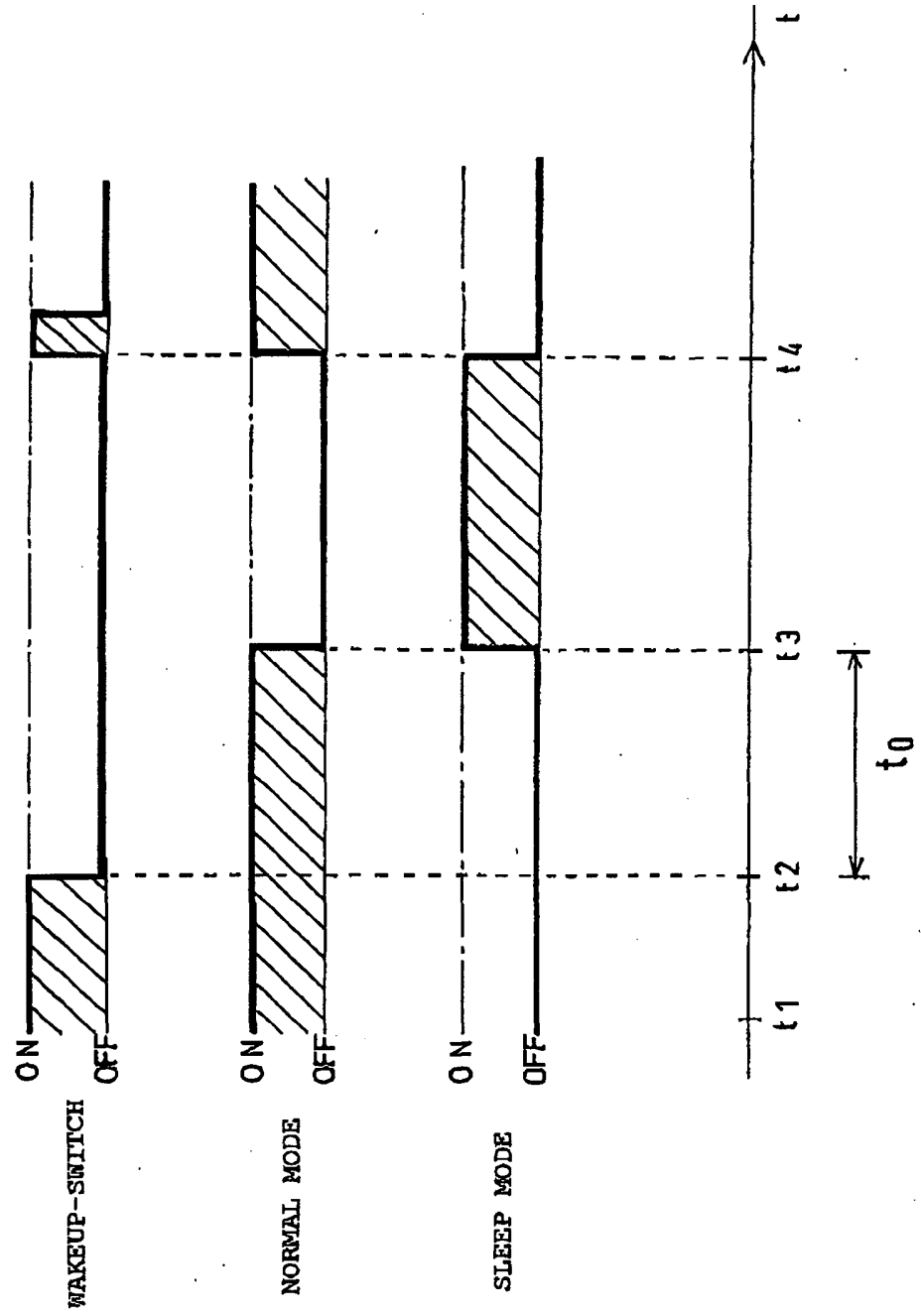


FIG. 8

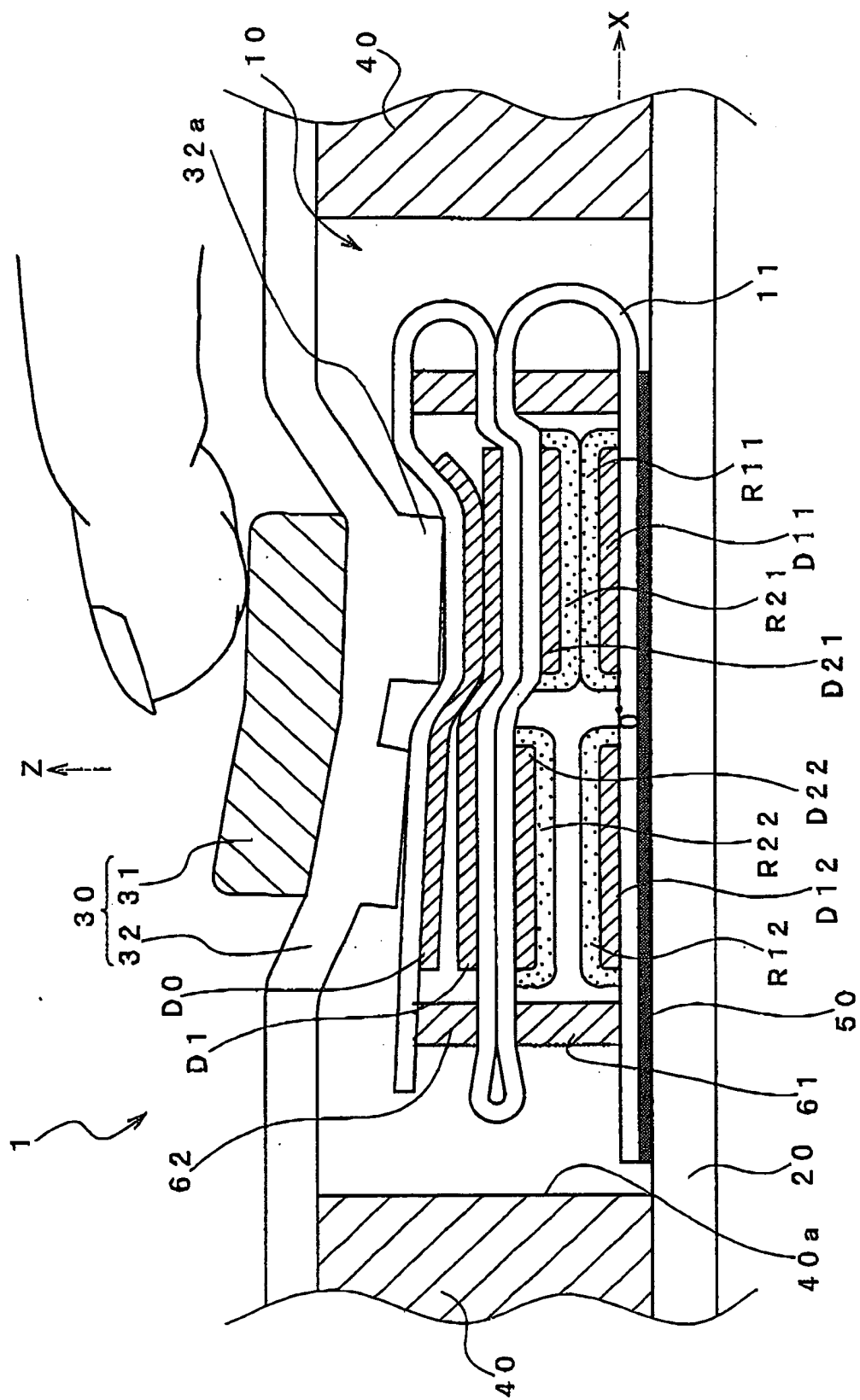


FIG. 9

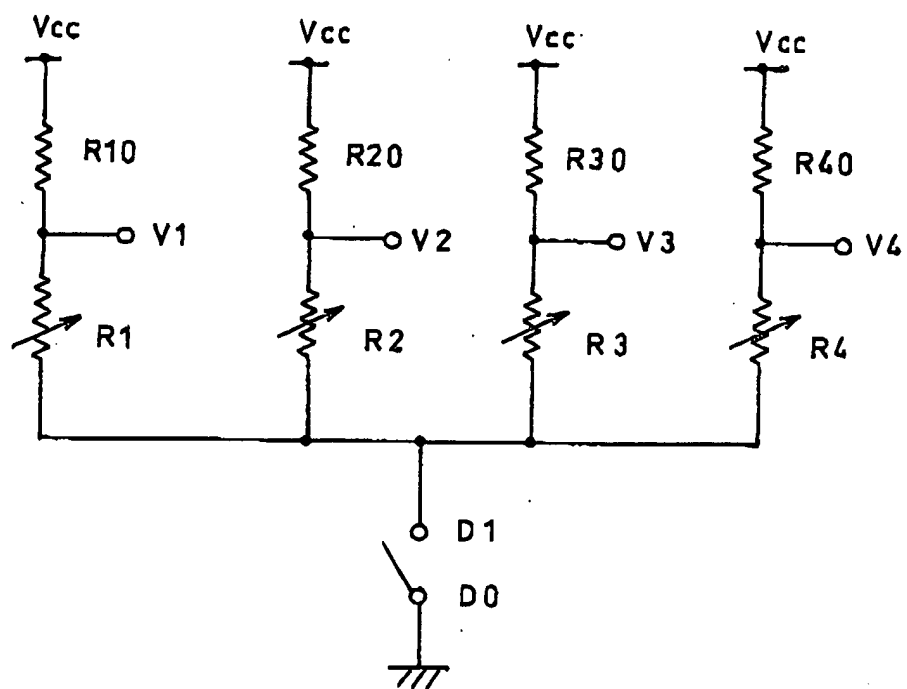


FIG. 10

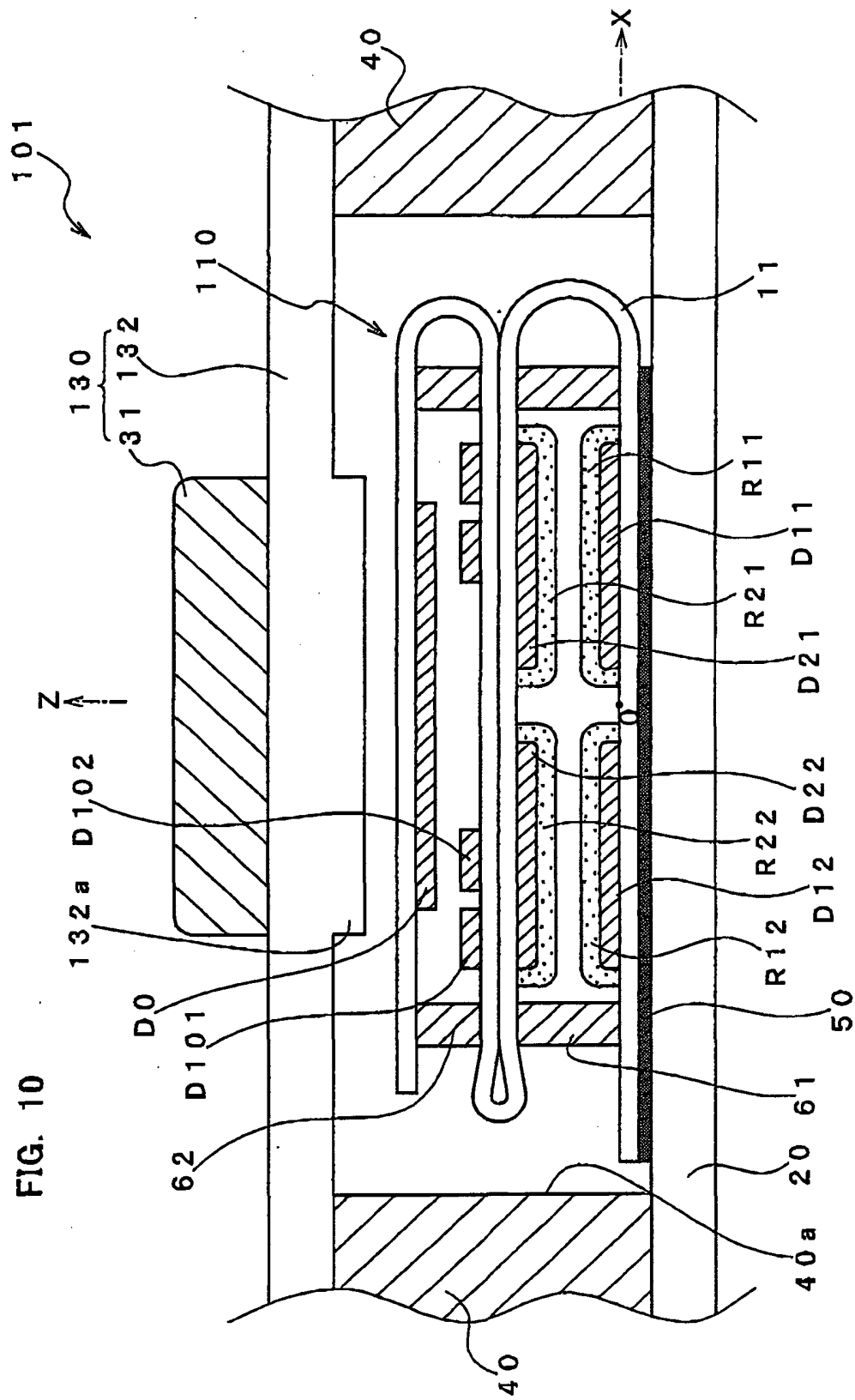


FIG. 11

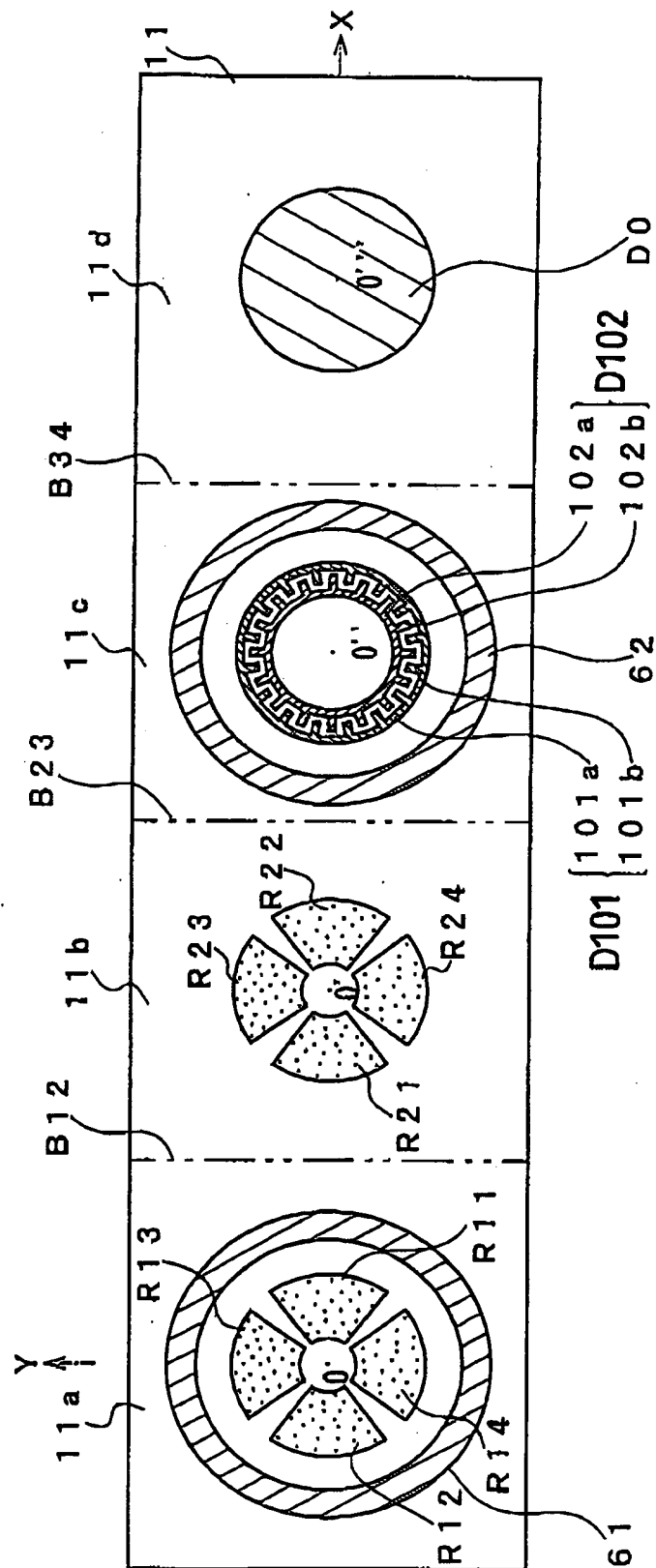


FIG. 12

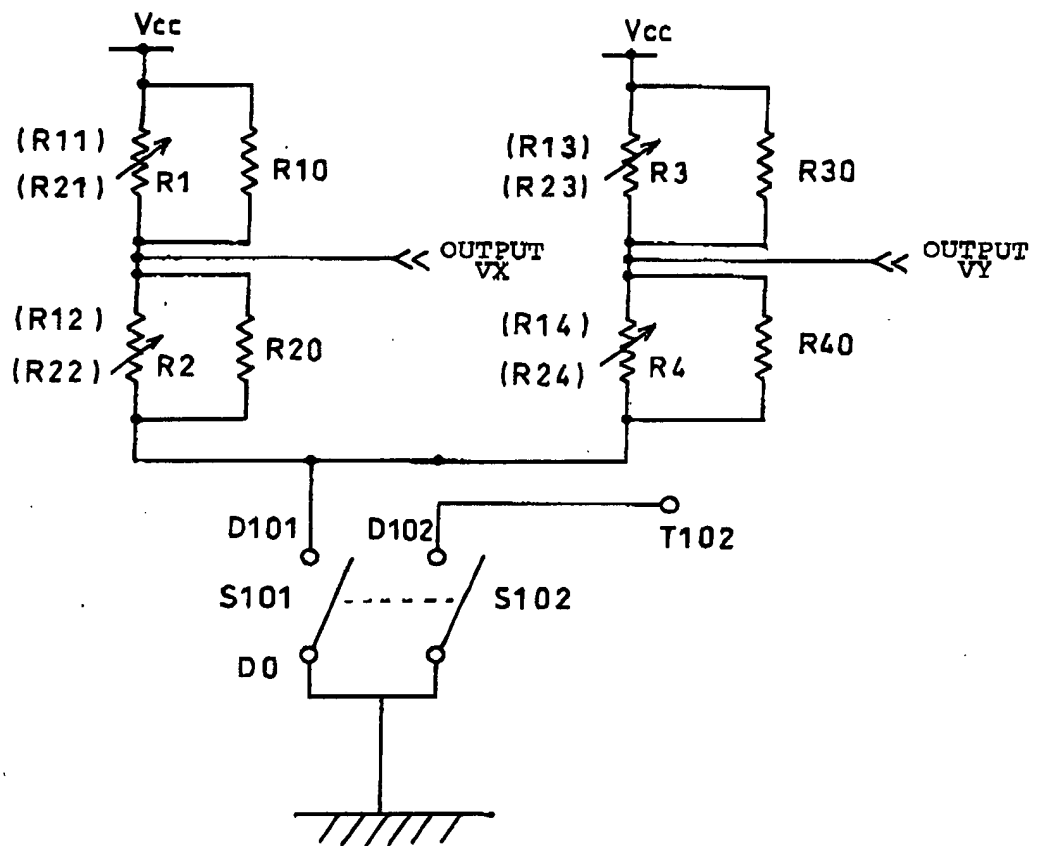


FIG. 13

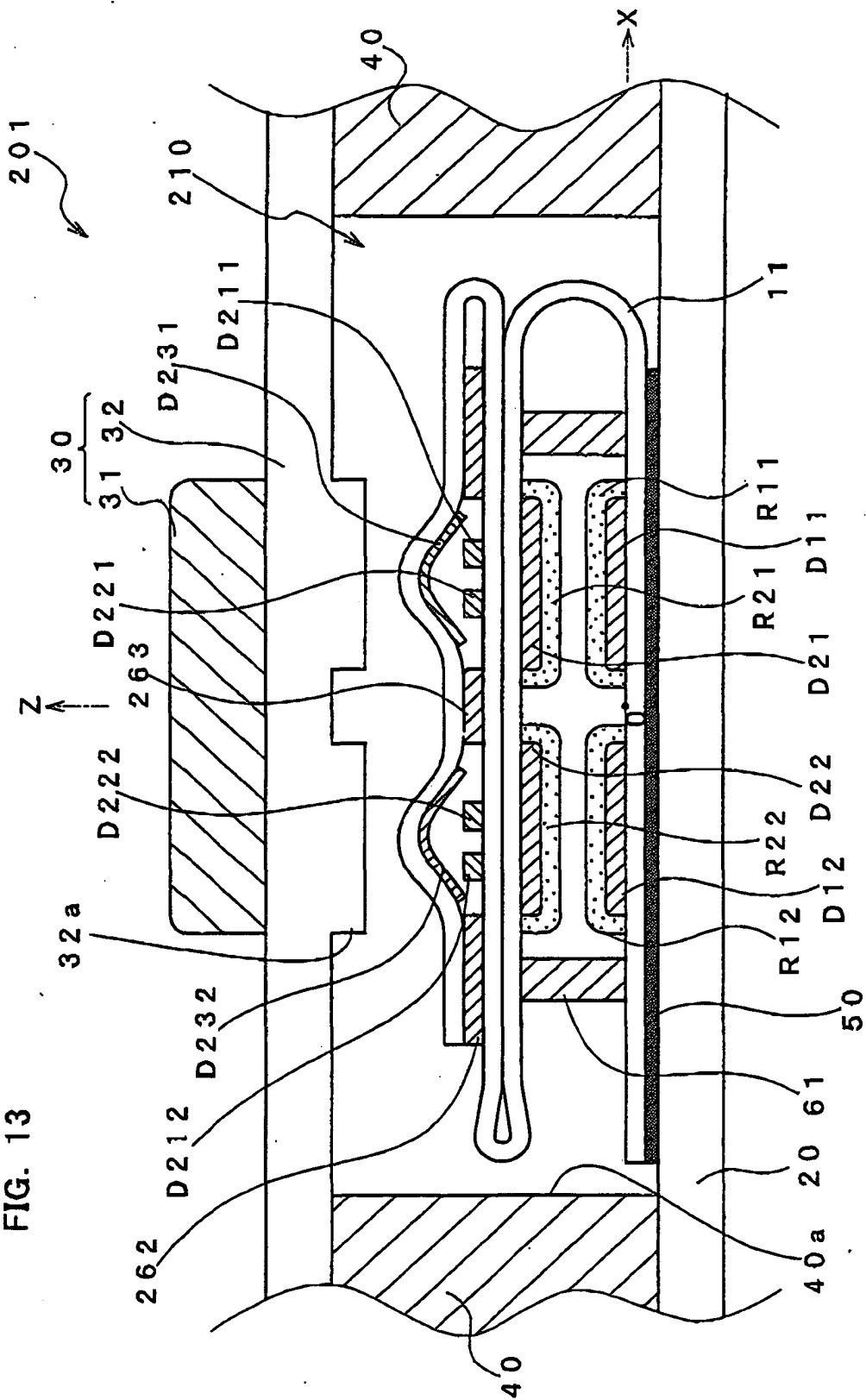


FIG. 14

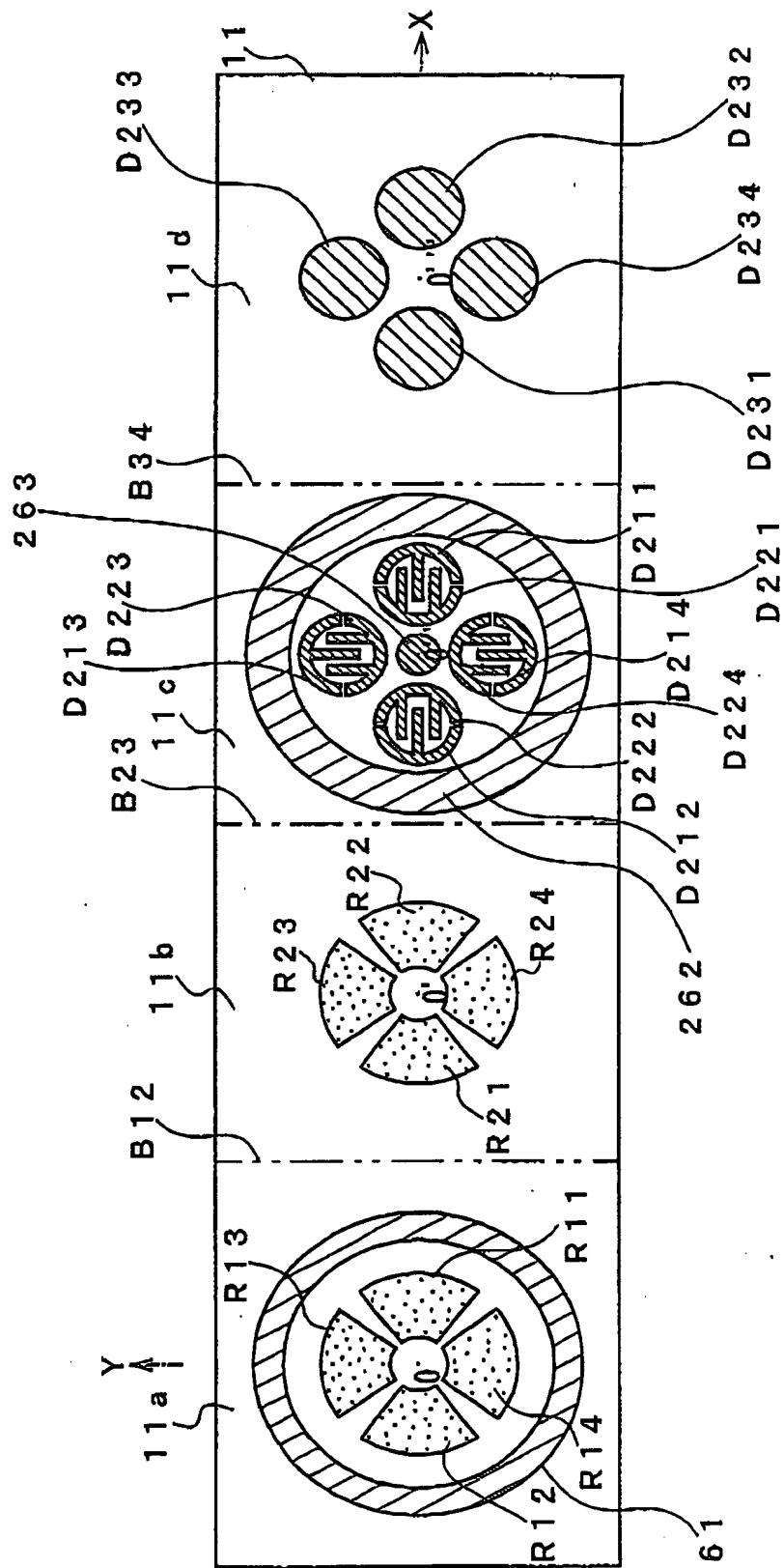


FIG. 15

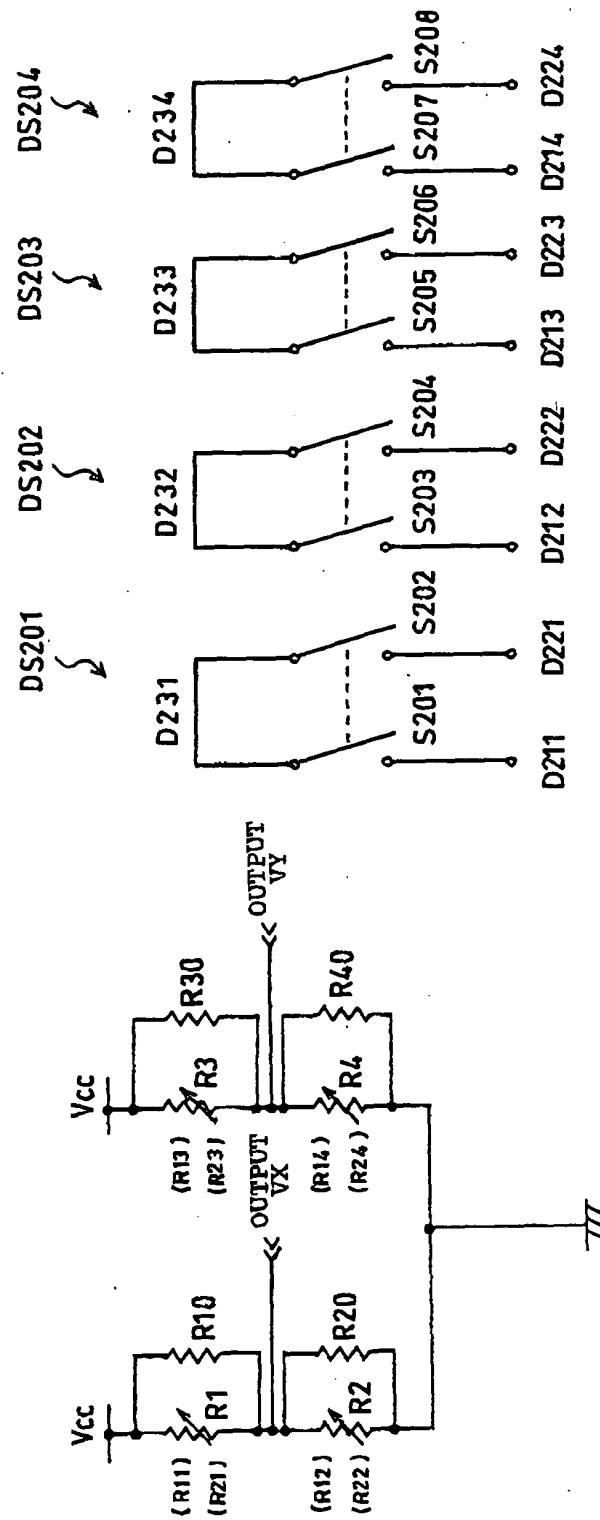
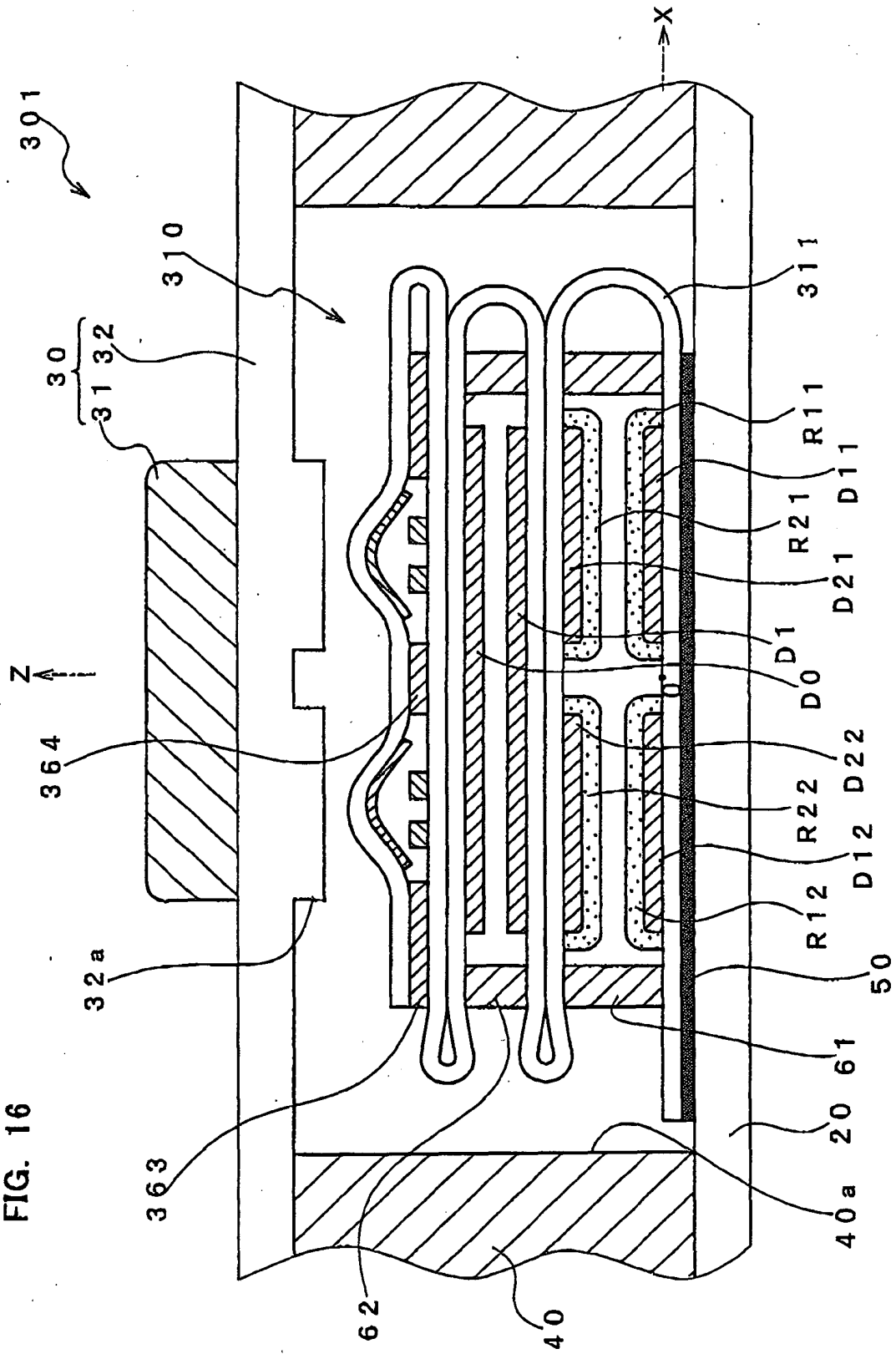


FIG. 16



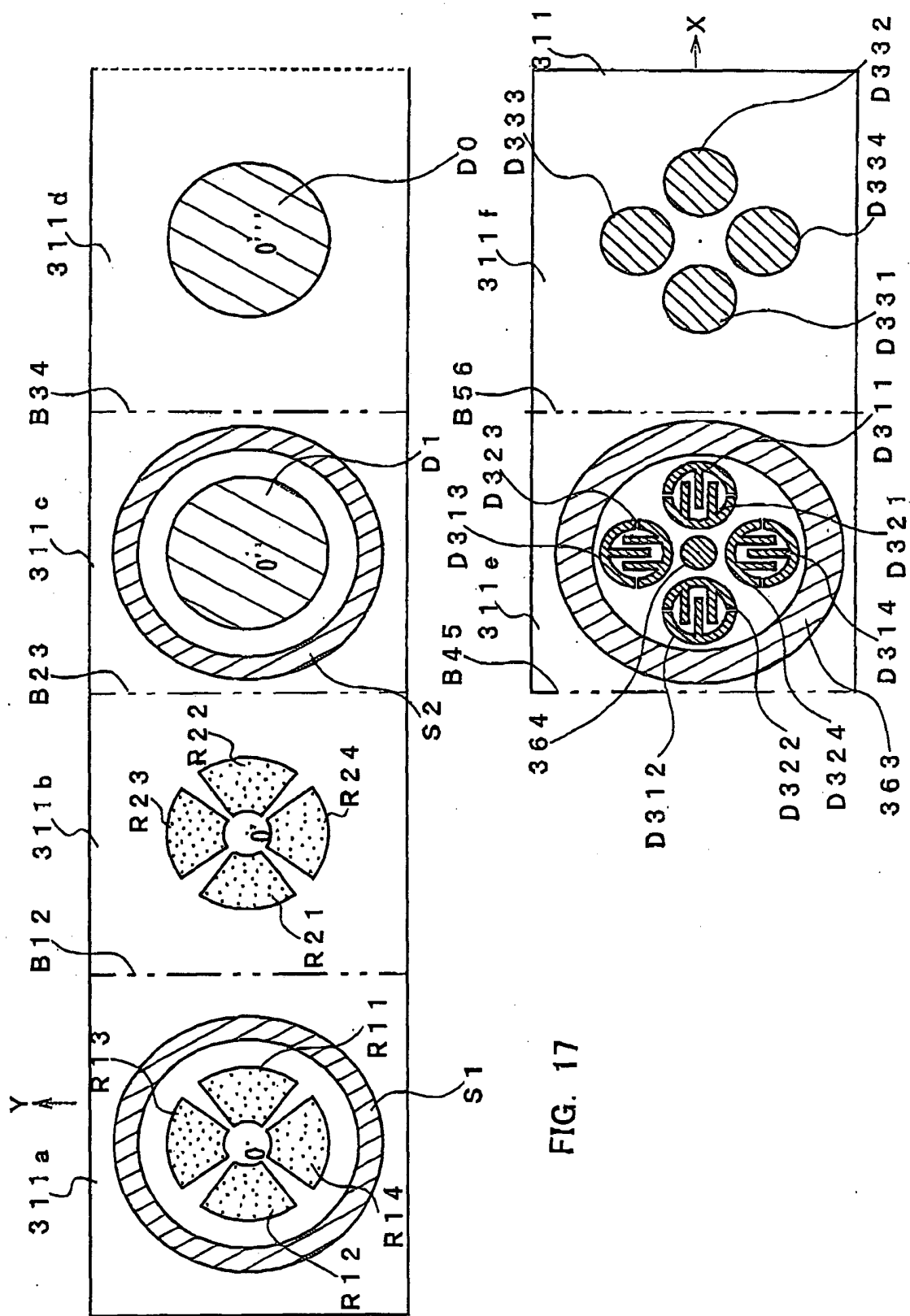


FIG. 17

FIG. 18

